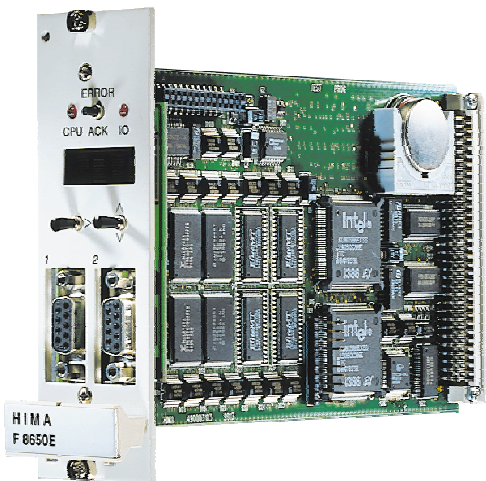


H41q/H51q



HI Quad Technology

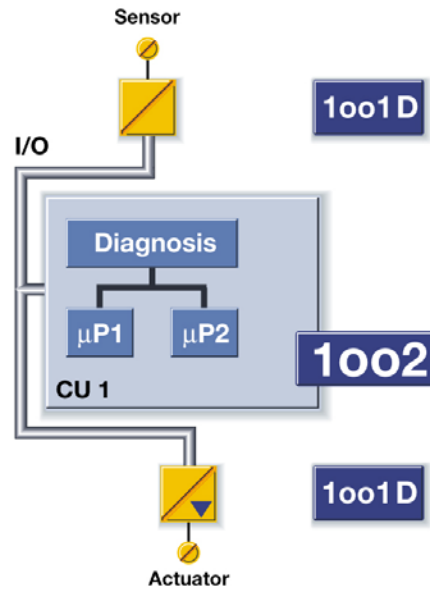
- ➔ 2oo4D/QMR (Quadruple Modular Redundant) architecture
- ➔ Double processors on each CPU
- ➔ Both processors are constantly checked by a hardware comparator in a matter of nanoseconds
- ➔ In redundant architectures, a Dual Port RAM (DPR) establishes communication with the second central processing unit
- ➔ This means that safety is no longer a topic for discussion. Configuration decisions are reduced to availability requests



H41q/H51q

PES: H41q/H51q.

→ SIL 3 – Standard availability.



H41q/H51q

PES: H41q/H51q.

→ SIL 3 – High availability.

